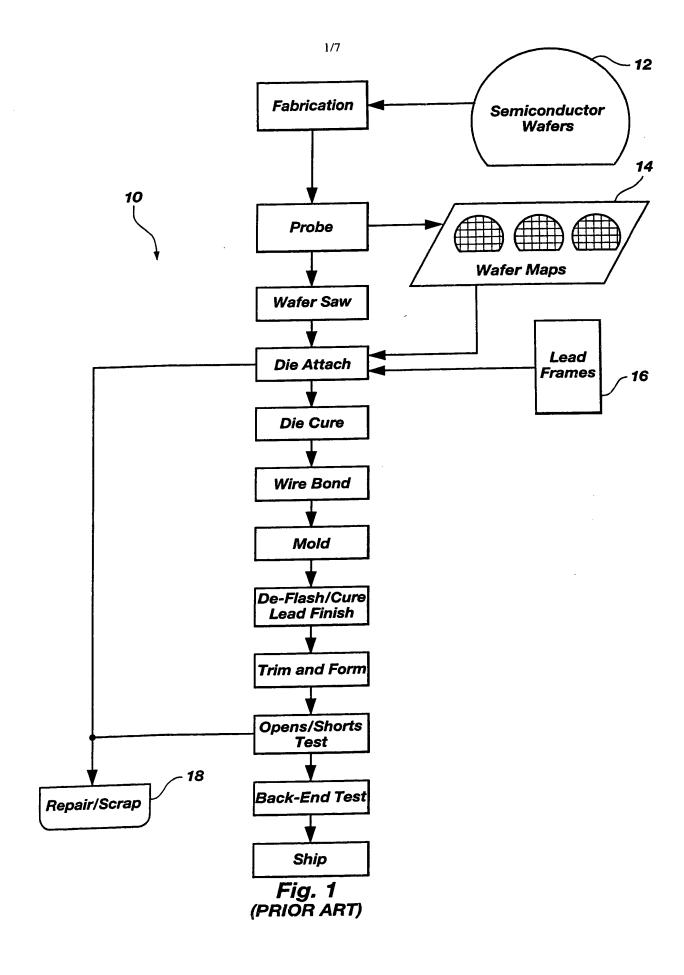
TITLE: NON-LOT BASED METHOD FOR ASSEMBLING INTEGRATED CIRCUIT DEVICES

Inventor: Wilson et al. Docket No.: 3247.4US



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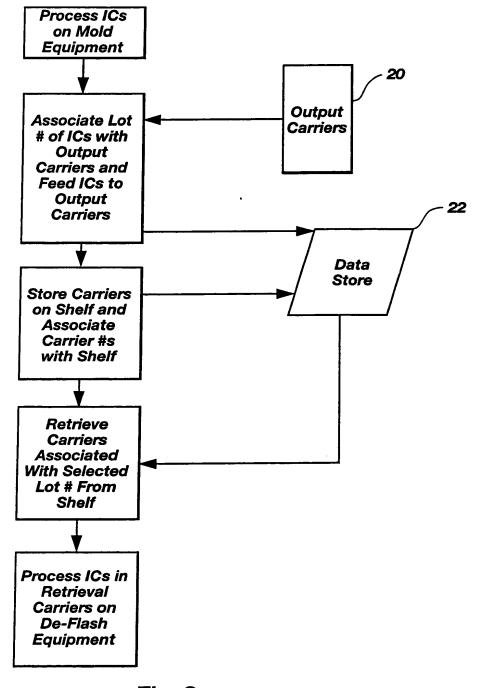


Fig. 2 (PRIOR ART)

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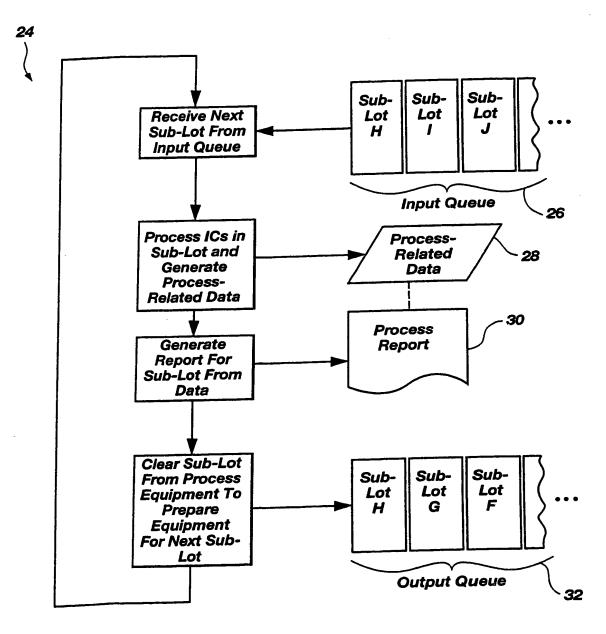


Fig. 3 (PRIOR ART)

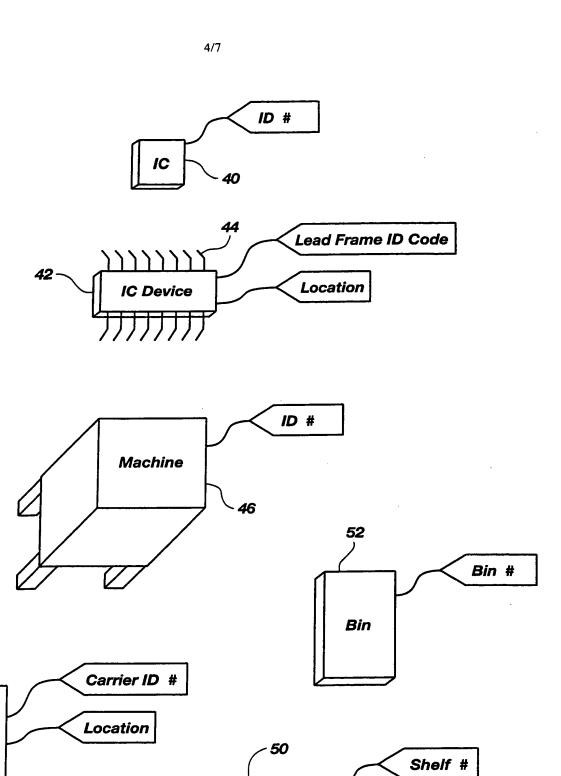


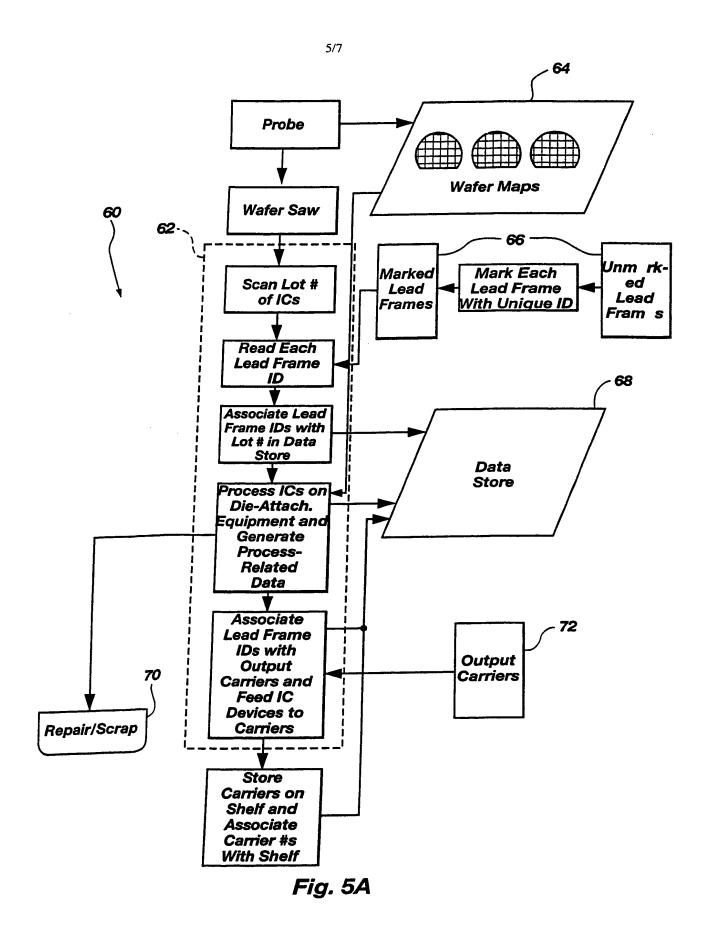
Fig. 4

Sh If

48

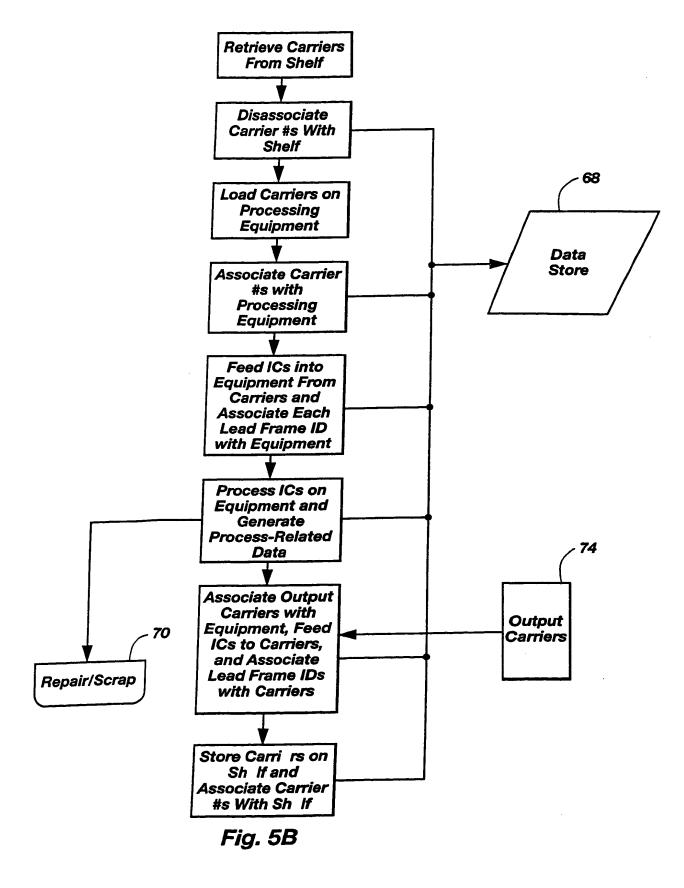
Carrier

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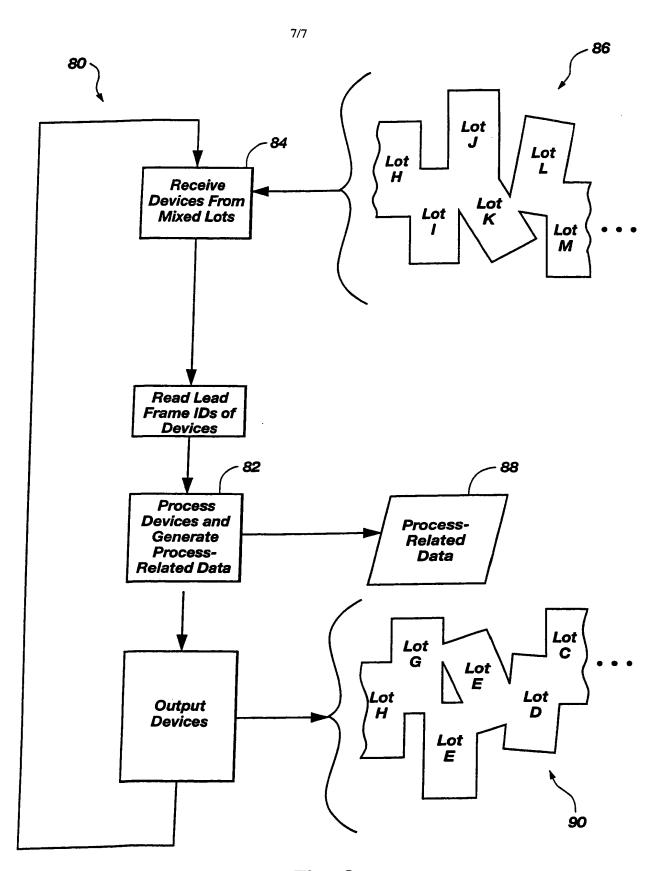


Fig. 6